

Amendments to the Claims:

This listing of the claims will replace all prior versions and listings of claims in the application.

1. (Currently Amended) A method of emulating a switched Ethernet local area network in a computing platform having a plurality of computer processors, and a non-Ethernet switch fabric and with point-to-point links between the non-Ethernet switch fabric and to the computer processors, comprising:

defining a switched Ethernet network topology of to be emulated on the computing platform, the topology including processor nodes and a switch node;
assigning a set of computer processors to act as the processor nodes;
assigning a computer processor to act as the switch node, said switch node emulating an Ethernet switch;

providing Ethernet driver emulation logic to execute on-at least two the processor nodes computer processors;

providing switch emulation logic to execute on the switch node at least one of the computer processors;

establishing a first set of virtual interfaces with the non-Ethernet switch fabric, said first set of interfaces being between the switch emulation logic on the switch node and each computer processor node having Ethernet driver emulation logic executing thereon to allow software communication therebetween, wherein each virtual interface of the first set defines a an indirect software communication path from one computer processor node to another computer processor node via the non-Ethernet switch fabric and via the switch node;

establishing a second set of virtual interfaces with the non-Ethernet switch fabric, said second set of interfaces being between each computer processor node having Ethernet driver emulation logic executing thereon and every other computer processor node, having Ethernet driver emulation logic executing thereon wherein each virtual interface of the second set defines a direct software communication path from one processor node to another processor node via the non-Ethernet switch fabric while avoiding the switch node;

if the a virtual interface from the second set of interfaces between one computer processor and another is operating to satisfy predetermined criteria, the Ethernet driver emulation logic of the one ~~computer~~ processor node unicast communicating with the other ~~computer~~ processor node via a the virtual interface from the second set of interfaces defining a software communication path therebetween; and

if the virtual interface from the second set of interfaces between one computer processor and another is operating to not satisfy predetermined criteria, the Ethernet driver emulation logic of the one ~~computer~~ processor node unicast communicating with the other ~~computer~~ processor node via a virtual interface from the first set of interfaces to the switch emulation logic which transmits the unicast communication to the other computer processor.

2. (Currently Amended) The method of claim 1 wherein each of the processor nodes ~~computer processors having Ethernet driver emulation logic executing thereon~~ is associated with a virtual MAC address and wherein the MAC addresses are formed according to rules to identify the computer processor acting as the processor nodes as one of the plurality of computer processors of the computing platform distinct from MAC addresses of an external network.

3. (Original) The method of claim 2 wherein the platform is connected to an external network via interface logic for communicating with an external network, wherein the external network interface logic is associated with its own MAC address, and wherein messages are communicated on the external network using the MAC address of the external network interface logic.

4. (Currently Amended) The method of claim 1 wherein a first ~~computer~~ processor node uses a first virtual interface to unicast communicate with a second ~~computer~~ processor node but wherein the second ~~computer~~ processor node uses a different virtual interface to communicate to the first ~~computer~~ processor node.

5. (Original) The method of claim 1 wherein each computer processor includes switch fabric driver logic for communicating on the point to point links and that includes check

summing capability and wherein the Ethernet driver emulation logic includes check summing capability but disables such check summing if the switch fabric driver logic has already check summed a message.

6. (Original) The method of claim 5 wherein the switch fabric driver logic implements a reliable communication protocol to ensure reception of messages over the switch fabric.

7. (Original) The method of claim 1 wherein the switch fabric and point to point links are arranged in a redundant configuration.

8. (Original) The method of claim 1 wherein the Ethernet driver emulation logic broadcast communicates a message by sending the message to the switch emulation logic via a virtual interface and wherein the switch emulation logic receives and clones a broadcast message from a virtual interface and transmits the cloned message to other computer processors in the network.

9. (Original) The method of claim 1 wherein the switch emulation logic defines and maintains computer processor membership to an emulated network.

10. (Original) The method of claim 1 wherein the Ethernet driver emulation logic transmits messages larger than maximum transmission unit (MTU) size.

11. (Currently Amended) A system for emulating a switched Ethernet local area network, comprising:

a plurality of computer processors with Ethernet switch emulation logic on one computer processor and Ethernet driver emulation logic on other computer processors;
a non-Ethernet switch fabric and point-to-point links to the processors;
virtual interface logic to establish a first set of virtual interfaces over with the non-Ethernet switch fabric and point to point links, wherein each virtual interface of the first set of interfaces defines a an indirect software communication path from one computer processor to another computer processor via the switch fabric and via the computer processor having the Ethernet switch emulation logic thereon

and to establish a second set of virtual interfaces with the non-Ethernet switch fabric, wherein each virtual interface of the second set defines a direct software communication path from one computer processor to another via the non-Ethernet switch fabric while avoiding the computer processor having the Ethernet switch emulation logic thereon;

~~Ethernet driver emulation logic, executing on at least two computer processors;~~
~~switch emulation logic, executing on at least one of the computer processors, including~~
~~logic to establish a virtual interface between the switch emulation logic and each~~
~~computer processor having Ethernet driver emulation logic executing~~
~~thereon to allow software communication therebetween;~~
~~logic to receive a message from one of the virtual interfaces to a computer~~
~~processor having Ethernet driver emulation logic executing thereon and to~~
~~transmit a message to another computer processor having Ethernet driver~~
~~emulation logic executing thereon, in response to addressing information~~
~~associated with the message; and~~
~~logic to establish a virtual interface between each computer processor having~~
~~Ethernet driver emulation logic executing thereon and every other~~
~~computer processor having Ethernet driver emulation logic executing~~
~~thereon;~~

wherein the Ethernet driver emulation logic includes logic to unicast communicate with another computer processor in the emulated Ethernet network via a virtual interface of the second set of interfaces defining a software communication path therebetween if the virtual interface is operating to satisfy predetermined criteria, and to unicast communicate with the other computer processor via a virtual interface of the first set of interfaces the switch emulation logic if the virtual interface of the second set is not operating to satisfy predetermined criteria.

12. (Original) The system of claim 11 wherein each of the computer processors having Ethernet driver emulation logic executing thereon is associated with a virtual MAC address and wherein the MAC addresses are formed according to rules to identify the computer processor as one of the plurality of computer processors distinct from MAC addresses of an external network.

13. (Original) The system of claim 12 further comprising external network interface logic for communicating with an external network, wherein the external network interface logic is associated with its own MAC address, and wherein the switch emulation logic includes logic for sending messages to the external network interface logic for communication on to the external network, wherein such messages are communicated on the external network using the MAC address of the external network interface logic.

14. (Original) The system of claim 11 wherein a first computer processor uses a first virtual interface to unicast communicate with a second computer processor but wherein the second computer processor uses a different virtual interface to communicate to the first computer processor.

15. (Original) The system of claim 11 wherein each computer processor includes switch fabric driver logic for communicating on the point to point links, and wherein the switch fabric driver logic includes check summing capability and wherein the Ethernet driver emulation logic includes check summing capability and includes logic to disable check summing within the Ethernet driver emulation logic if the switch fabric driver logic has check summed a message.

16. (Original) The system of claim 15 wherein the switch fabric driver logic implements a reliable communication protocol to ensure reception of messages over the switch fabric.

17. (Original) The system of claim 11 wherein the switch fabric and point to point links are arranged in a redundant configuration.

18. (Original) The system of claim 11 wherein the Ethernet driver emulation logic includes logic to broadcast communicate a message by sending the message to the switch emulation logic via a virtual interface and wherein the switch emulation logic includes broadcast logic to receive and clone a broadcast message from a virtual interface and to transmit the cloned message to other computer processors in the network.

19. (Original) The system of claim 11 wherein the switch emulation logic includes logic to define and maintain computer processor membership to an emulated network.

20. (Original) The system of claim 11 wherein the Ethernet driver emulation logic includes logic to transmit messages larger than maximum transmission unit (MTU) size.